



## UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: ACHLER Serial No.: 10/026,370 Filed: December 17, 2001  Title: ARCHITECTURES FOR A MODULARIZED DATA OPTIMIZATION ENGINE AND METHODS THEREFOR	Group Art Unit: 2164 Examiner: ORTIZ, BELIX M Docket: ANTAP001/IC-0102 Confirmation No.: 8038
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**INFORMATION DISCLOSURE STATEMENT****US PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant	Reference to Related Case

**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date	Applicant	Reference to Related Case	T

**OTHER DOCUMENTS**

Examiner Initials	Cite No.		T
bno		Robert W. Kembel; Fibre Channel: A Comprehensive Introduction", Northwest Learning Associates, Inc., Tucson, AZ 2000	
bno		Mark Nelson; "LZW Data Compression", <a href="http://www.dogma.net/markn/articles/lzw/lzw.htm">http://www.dogma.net/markn/articles/lzw/lzw.htm</a> , Originally published in October 1989 Dr. Dobb's Journal (2800 Campus Drive, San Mateo, CA. www.ddj.com)	
bno		Terry A. Welch; "A Technique for High Performance Data Compression", IEEE Computer 17(6), June 1984, pp. 8-19	

Examiner Signature	<i>Belix M. Ortiz</i>	Date Considered	<i>2/2/05</i>
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**OTHER DOCUMENTS**

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bmo		US Patent Application No.: 10/026,680 Filed: December 17, 2001 Title: "Integrated circuits for a high speed adaptive compression and methods therefor" Docket: ANTAP002/IC-0103	
bmo		US Patent Application No.: 10/026,679 Filed: December 17, 2001 Title: "Interface circuits for modularized data optimization engines and methods therefor" Docket: ANTAP004/IC-0105	
bmo		US Patent Application No.: 10/026,368 Filed: December 17, 2001 Title: "Interface receive circuits for modularized data optimization engines and methods therefor" Docket: ANTAP005/IC-0106	

Examiner Signature	Belix M. Ortiz	Date Considered	2/2/05
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